

Figure 1(Prior Art)

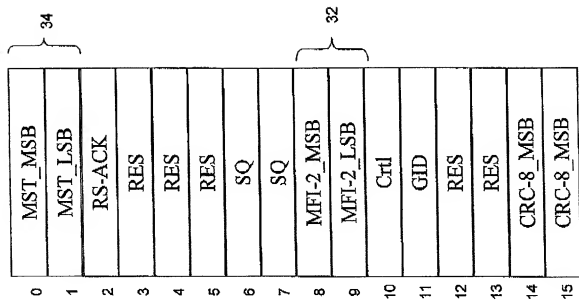


Figure 2 (Prior Art)

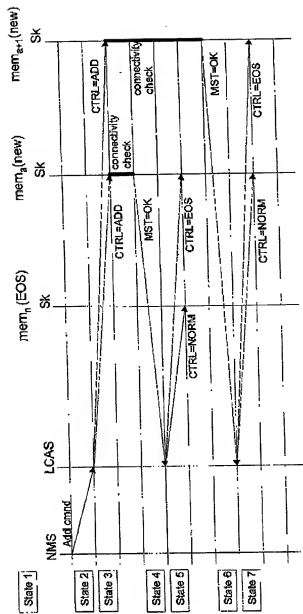


Figure 3 (Prior Art)

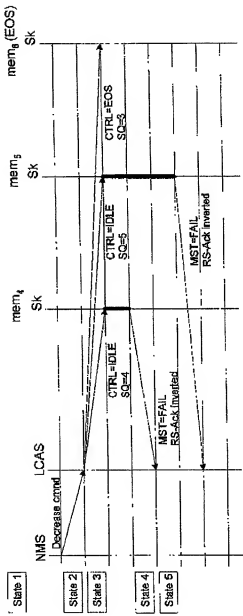


Figure 4 (Prior Art)

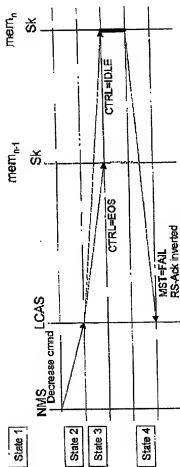


Figure 5 (Prior Art)

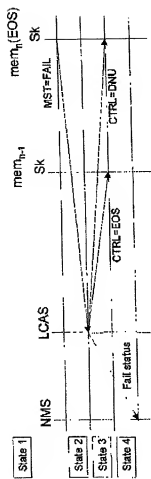


Figure 6 (Prior Art)

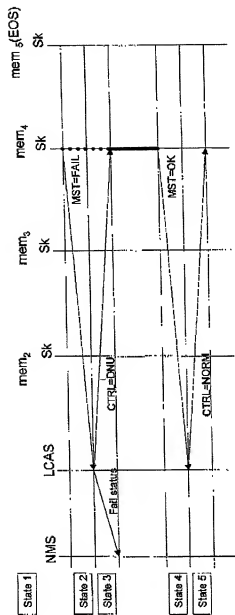


Figure 7 (Prior Art)

Multi-Frame No	RTN Channel #1	RTN Channel #2
1	0-7	128-135
2	8-15	136-143
3	16-23	144-151
....
29	224-231	96-103
30	232-239	104-111
31	240-247	112-119
32	248-255	120-127

Figure 8

Mem #1 Mem #2 Mem #3

2ms	0-7	16-23	8-15
	8-15	0-7	16-23
	16-23	8-15	0-7
	0-7	16-23	8-15
	8-15	0-7	16-23
	16-23	8-15	0-7

Detailed description: The diagram illustrates a memory access pattern over a 2ms duration. It features a 6x4 grid of cells. The first column is labeled '2ms' with a bracket indicating the duration. The subsequent three columns are labeled 'Mem #1', 'Mem #2', and 'Mem #3'. Each cell in the grid contains a range of values: '0-7', '8-15', or '16-23'. The values in each column follow a repeating pattern of 0-7, 8-15, and 16-23. Dashed lines extend from the right side of the grid.

Figure 9

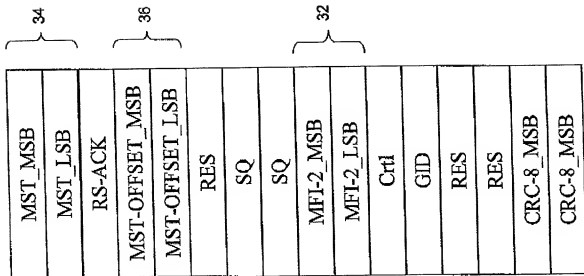


Figure 10

Figure 11 (a) Prior Art

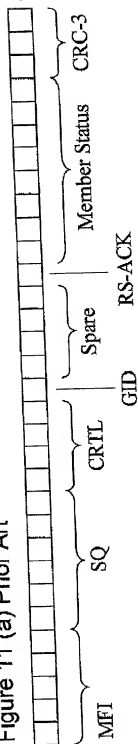


Figure 11 (b)

